ABSTRACT OF THE DISCLOSURE

This invention discloses an arrangement for suppressing an erroneous display and suppressing damage to an image display apparatus when a power source is turned on, the power source is turned off, an outlet is removed, or power fails. Particularly, this invention discloses an arrangement for stopping, for a predetermined time, input of a scanning signal or modulation signal to a display panel or application of an accelerating potential in turning on the power source. This invention discloses an arrangement for stopping input of a scanning signal or modulation signal to the display panel, and then stopping supply of power in turning off the power source.

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